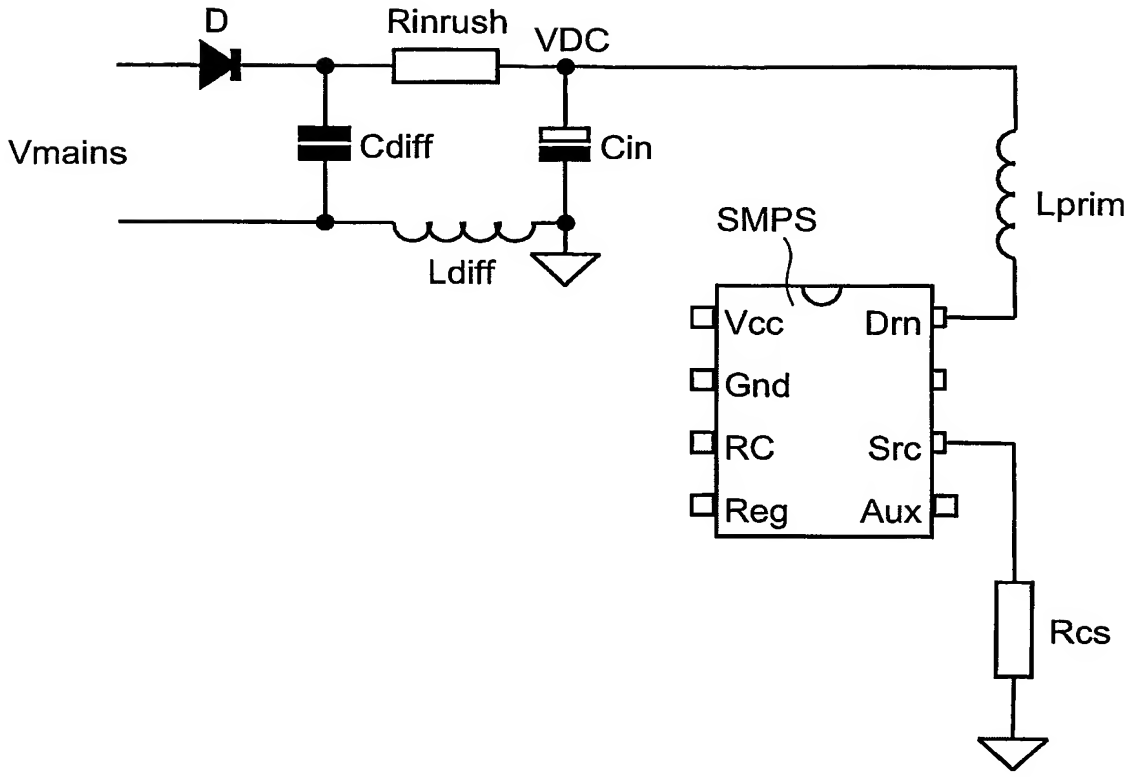
D
Rinrush
VDC
Vmains
Cdiff
Cin
Lprim
SMPS
Ldiff
Vcc
Drn
Gnd
RC
Src
Reg
Aux
Rcs

FIG.1